

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(encoder and (off adj chip adj resistor) and shift\$3 and (mux or multiplexer) and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:44
L2	2	(encoder and (resistor) and shift\$3 and (mux or multiplexer) and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:43
L3	0	(transistor and (off adj chip adj resistor) and shift\$3 and (impedance) and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:45
L4	0	(transistor and (off adj chip adj resistor) and shift\$3 and (impedance) and (mux or multiplexer) and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:45
L5	0	(transistor and (resistor) and shift\$3 and (impedance) and encoder and converter and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:46
L6	0	(transistor and (resistor) and shift\$3 and (impedance) and encoder and analog and digital and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:46
L7	0	((resistor) and shift\$3 and (impedance) and encoder and analog and digital and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:47
L8	0	((resistor) and (impedance) and encoder and analog and digital and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:47
L9	0	((resistor) and (impedance) and encoder and adc and terminat\$3).clm.	US-PGPUB	OR	ON	2007/04/07 21:47